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In re Application of:

SPENCER A. PETERSEN
PATRICK F. O'CONNELL, II
TERENCE M. CLIFTON

Serial No.: 09/915,792

Filed: July 26, 2001

For: METHOD AND APPARATUS FOR
DETERMINING CRITICAL TIMING
PATH SENSITIVITIES OF MACROS IN A
SEMICONDUCTOR DEVICE

Group Art Unit: 2825

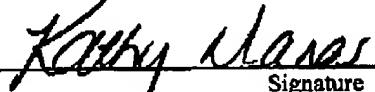
Examiner: M. Dimyan

Atty. Dkt. No.: 2000.049900

OFFICIAL

RESPONSE TO RESTRICTION
REQUIREMENT DATED JULY 9, 2004Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF TRANSMISSION
37 C.F.R. 1.6(d)I hereby certify that this paper or fee is being transmitted to the
Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450
at 1-703-672-9306 on this the 21st day of July, 2004.

Signature

This paper is submitted in response to the Restriction Requirement dated July 9, 2004 for which the date for response is August 9, 2004.

In response to the restriction requirement that the Examiner imposed, Applicants elect, with traverse, to prosecute claims 1-8 and 16, i.e., the Group I claims.

Applicants also note that claim 16 is a claim to "means" for practicing the method claims set forth in the Group I claims. According to MPEP § 806.05(e), a "means" claim is a linking claim and must be examined with the elected invention. If it is ultimately allowed, rejoinder of

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GROUP ART UNIT 2825

DATE: JULY 21, 2004

TO: EXAMINER M. DIMYAN TOTAL NO. OF PAGES INCLUDING COVER: 3

FAX: 703.872.9306 PHONE:

FROM: KATHRYN DANAS

RE: RESPONSE TO RESTRICTION FILE: 2000.049900
REQUIREMENT TT3951
09/915,792

 URGENT FOR REVIEW PLEASE YOUR FILE PLEASE REPLY PLEASE HANDLE
ORIGINAL: WILL FOLLOW WILL NOT FOLLOW

NOTES/COMMENTS:

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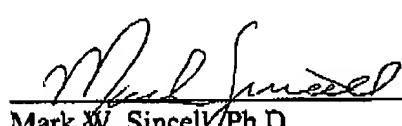
the non-elected claims is required. See MPEP § 809.04. Claim 16 satisfies the criteria set forth in MPEP 806.05(e) for a linking claim. Thus, if claim 16 is found to be allowable, Applicants are entitled to have the remaining Group II claims (i.e., 9-15) rejoined.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3951. If funds are insufficient, the Commissioner is authorized to deduct said fees from Williams, Morgan & Amerson's P.C. Deposit Account 50-0786/2000.049900.

The Examiner is invited to contact the undersigned at (713) 934-4052 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

Date: 7/21/04



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